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»

JF Croix, DF Wong - DAC, 2003 - doi.ieeecomputersociety.org  
 ... Using Current-Based Models John F. Croix Silicon Metrics Corporation 12710 Research  
 Blvd. Suite 300 Austin, Texas 78759 **John.Croix@siliconmetrics.com** ...  
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JF Croix, DF Wong - DESIGN AUTOMATION CONFERENCE, 1997 - doi.ieeecomputersociety.org  
 Page 1. A Fast And Accurate Technique To Optimize Characterization Tables For  
 Logic Synthesis John F. Croix Advanced Micro Devices, Inc. ...  
 Cited by 6 - [Web Search](#) - [BL Direct](#)

[CITATION] The Need for Accurate Power Models for Deep Submicron IP reuse

JF Croix - Electronic Systems, 1999  
 Cited by 3 - [Web Search](#)

West Indian Migration to the United States Virgin Islands: Demographic Impacts and Socioeconomic ... - group of 3 »

K de Albuquerque, JL McElroy - International Migration Review, 1982 - JSTOR  
 ... Virgin Islands Immigration Thomas **John Croix** Total Before 1930 386 8 182 576  
 1930-39 134 8 26 168 1940-49 250 13 40 303 1950-54 322 14 113 449 1955-59 735 52 ...  
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D MALINIAK - ELECTRONIC DESIGN, 2001 - mda.cuesta.com  
 ... in the context of the design itself, not just a priori like a standard-cell library  
 is characterized before you even build a chip," says **John Croix**, CTO at ...  
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Leakage Power Estimation and Minimization in VLSI Circuits - group of 2 »

WT Shiue - IEEE INTERNATIONAL SYMPOSIUM ON CIRCUITS AND SYSTEMS, 2001 - engr.oregonstate.edu  
 ... employees Guruprasad Rao, Vess Johnson, Stephen King, Tamara Cryar, Hope Luedecke,  
 Callan Carpenter, Shakir Abbas, **John Croix**, Scott Yore, and Paul Ballast. ...  
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LEAKAGE POWER ESTIMATION AND MINIMIZATION IN VLSI CIRCUITS

TX Austin - ieexplore.ieee.org  
 ... Vess Johnson. Stephen King, Tamara Cryar. Hope Luedecke. Callan Carpenter. Shakir  
 Abbas, **John Croix**. Scott Yore. and Paul Ballast. 7. REFERENCES Z. Chen. ...  
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G De Micheli - ieexplore.ieee.org  
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CL Ratzlaff, LT Pillage - IEEE Transactions on Computer-Aided Design of Integrated ..., 1994 -  
ieeexplore.ieee.org

Page 1. IEEE TRANSACTIONS ON COMPUTER-AIDED DESIGN OF INTEGRATED CIRCUITS AND  
SYSTEMS, VOL. 13, NO. 6, JUNE 1994 163 RICE: Rapid Interconnect ...

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S Ghiasi - 1999 - csel.cs.colorado.edu

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Ghiasi BA, University of Colorado at Boulder, 1996 A thesis submitted to the ...

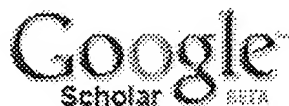
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 ... Suite 300 Austin, Texas 78759 [John.Croix@siliconmetrics.com](mailto:John.Croix@siliconmetrics.com) ... models were created for  
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